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7590 04/28/2005

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Joseph P. Fox

(Depositor's name)

July 26, 2005

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/706,664	11/12/2003	Yoshihiro Kizaki	0610.68726	8620

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/28/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
ZWEIZIG, JEFFERY SHAWN	2816	327-543000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).	2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
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(A) NAME OF ASSIGNEE

FUJITSU LIMITED

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

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Joseph P. Fox

Date July 26, 2005

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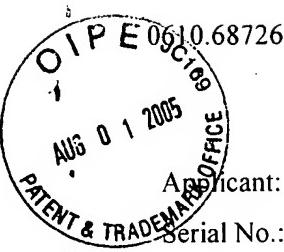
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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshihiro Kizaki
 Serial No.: 10/706,664
 Conf. No.: 8620
 Filed: 11/12/2003
 For: SEMICONDUCTOR INTEGRATED CIRCUIT
 Art Unit: 2816
 Examiner: Zweizig, Jeffery Shawn

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

7/26/05

Date
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Appt. February 20, 1998

Registration No. 41,760

Attorney for Applicant(s)

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 Alexandria, VA 22313-1450

Sir:

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Respectfully submitted,
GREER, BURNS & CRAIN, LTD.

By

Joseph P. Fox
Registration No. 41,760

July 26, 2005

300 South Wacker Drive, Suite 2500
 Chicago, Illinois 60606
 (312) 360-0080
 Customer No. 24978